## JVC

## SERVICE MANUAL CASSETTE RECEIVER

## KS-FX915R/KS-FX815



| Difference point | RDS | Dimmer | Beep | LCD | Remocon |
| :---: | :---: | :---: | :---: | :---: | :---: |
| KS-FX915R | O | O | O | Color | O |
| KS-FX815 | X | X | X | Nega | X |

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## Safety precaution

> § CAUTION Burrs formed during molding may be left over on some parts of the chassis. Therefore, pay attention to such burrs in the case of preforming repair of this system.

## Location of main parts

■ Control unit


## Disassembly method

## Removing the front chassis (See Fig.1)

1. Remove two screws $\mathbf{A}$ and insert a screwdriver to the joints $\mathbf{a}$ on the side of the front chassis and two joints $\mathbf{b}$ on the right side, then detach the front chassis toward the front side.

Removing the heat sink (See Fig. 2 )

1. Remove the three screws $\mathbf{B}$ attaching the heat sink on the left side of the body, and remove the heat sink.


Fig. 1


Fig. 2


Fig. 3

## ■Removing the main amplifier board assembly

(See Fig. 4 and 5)

1. Remove the front chassis.
2. Remove the bottom cover.
3. Remove the two screws $\mathbf{C}$ attaching the main amplifier board assembly on the bottom of the body.
4. Remove the three screws $\mathbf{D}$ attaching the main amplifier board assembly on the back of the body.
5. Disconnect connector CP401 on the main amplifier board assembly from the cassette mechanism assembly.

## Removing the Cassette mechanism assembly

(See Fig.6)

1. Remove the front chassis.
2. Remove the bottom cover.
3. Remove the main amplifier board assembly.
4. Remove the four screws $\mathbf{E}$ attaching the Cassette mechanism assembly from the top cover.

## Removing the control switch board

(See Fig. 7 and 8 )

1. Remove the front chassis.
2. Remove the four screws F attaching the rear cover on the back of the front panel unit.
3. Remove the control switch board from the front panel unit.


Fig. 7


## <Removal of the cassette mechanism> <br> ■Removing the head amplifier board.

(See Fig. 1 and 2)

1. For the 6 pin wire extending from connector CN402 on the head amplifier board, disconnect it from the head relay board.
2. Disconnect the card wire from connector CN403 on the head amplifier board.
3. Remove the screw A attaching the head amplifier board.
4. Move the tab a as shown in Fig. 2 and remove the head amplifier board while moving it in the direction of the arrow.

## ■Removing the cassette mechanism assembly (See Fig. 1 to 3)

1. Disconnect the 6pin wire from connector CN402 and the card wire from CN403 on the head amplifier board (Refer to Fig. 1 and 2).
2. Remove the four screws B on the bottom of the cassette mechanism.


Fig. 1


Fig. 3

## Removing the head relay board

(See Fig.4)

1. Unsolder the soldering $\mathbf{b}$ on the head relay board.
2. Remove the screw $\mathbf{C}$ attaching the head relay board.
3. Remove the head relay board in the direction of the arrow while releasing the two joints $\mathbf{c}$.

## ■Removing the load arm (See Fig.5)

1. Remove the $\mathbf{E}$ washer attaching the load arm using a pincette or something like that and remove the spring d.
2. Move the part of the load arm marked $※$ upwards to release it from the axis of rotation. Then rotate the load arm in the direction of the arrow to remove it from the cach.

## ■Removing the sub chassis (See Fig.6)

- Prior to performing the following procedure, remove the head relay board.

1. Remove the screw $\mathbf{D}$ attaching the sub chassis.
2. Push the tab $\mathbf{e}$ in the direction of the arrow to detach the one side of the sub chassis. Then release the sub chassis from the tab $f$.

## Removing the cassette holder and the holder arm in the eject mode

(See Fig. 7 and 8)

- Prior to performing the following procedure, remove the head relay board, the load arm and the sub chassis.

1. Remove the screw $\mathbf{E}$ attaching the reinforce bracket.
2. Remove the reinforce bracket.
3. Push the tab $\mathbf{g}$ fixing the cassette holder in the direction of the arrow and open the cassette holder and the holder arm upward until they stop at an angle of 45 degrees. Move the two joints $\mathbf{h}$ to the side and remove the cassette holder and the holder arm from the shaft.


Fig. 4


Fig. 5


Fig. 7

## ■Removing the play head (See Fig.9)

- Prior to performing the following procedure, remove the head relay board and the sub chassis.

1. Remove the two screws $\mathbf{F}$ attaching the play head (The spring under the play head comes off at the same time).

## ■Removing the pinch roller ass'y

(See Fig.9)

- Prior to performing the following procedure, remove the head relay board and the sub chassis.

1. Push each tab $\mathbf{i}$ in the direction of the arrow and pull out the pinch rollers on both sides.

## ■Removing the reel disc board

(See Fig.10)

1. Unsolder the soldering j on the reel disc board.
2. Push the seven tabs $\mathbf{k}$ on the bottom of the cassette mechanism assembly in the direction of the arrow.

## ■Removing the motor and the sub motor

(See Fig. 10 and 11)

1. Unsolder the two soldering I of the motor and the sub motor.
2. Release the sub motor from the three tabs m. Push the sub motor upward and pull out it.
3. Remove the belt on the bottom of the cassette mechanism assembly and remove the two screws $\mathbf{G}$ attaching the motor.

ATTENTION: The motors can be detached before removing the load arm.


Fig. 8


Fig. 10


Fig. 11

## ■Removing the flywheel

(See Fig. 10 and 12)

1. Prior to performing the following procedure, remove the head relay board, the load arm, the sub chassis, the cassette holder, the holder arm and the reel disc board.
2. Remove the belt on the bottom of the cassette mechanism ass'y.
3. Remove the slit washer attaching the flywheel on the upper side of the cassette mechanism ass'y and pull out the flywheel downward. Then remove another flywheel in the same way.

ATTENTION: When reassembling, make sure to use a new slit washer.

## $\square$ Removing the reel disc ass'y( I )

(See Fig. 12 to 14)

- Prior to performing the following procedure, remove the head relay board, the load arm, the sub chassis, the cassette holder and the holder arm.

1. Disengage the part $\mathbf{n}$ inside of the reel driver which engages with the shaft, using a pincette or something like that. Then remove the reel driver from the shaft.
2. Remove the reel driver spring and the reel table.


Fig. 10


Fig. 12


Fig. 14

## ■Removing the reel disc ass'y(II)

(See Fig. 12 to 15)
ATTENTION: Prior to performing the following procedure, remove the reel disc ( I ).

1. Release the plate from the three tabs $\mathbf{o}$.
2. Push aside the gear over the reel table using a pincette or something like that.
3. Remove the reel disc ass'y (II) as with the reel disc ass'y (I).

ATTENTION: Do not break the front panel tab fitted to the metal cover.

Push aside the gear and reattach the reel disc Ass'y( I ).


Fig. 15

## Adjustment method

Test Instruments reqired for adjustment
1.Digital osclloscope(100MHz)
2.Frequency Counter meter
3.Electric voltmeter
4.Wow \& flutter meter
5.Test Tapes

VT724 --........... for DOLBY level measurement
VT739 -....-- For playback frequency measurement
VT712 --- For wow flutter \& tape speed measurement
VT703 ----------. For head azimuth measurement
6.Torque gauge $\cdots-\cdots-\cdots$.-... Cassette type for CTG-N (mechanism adjustment)

- Measuring conditions(Amplifier section)

Power supply voltage
DC14.4V(10.5~16V)
Load impedance $4 \Omega(2$ Speakers connection)
Line out
20k $\Omega$

- Standard volume position

Balance and Bass,Treble volume .Fader
:Center(Indication"0")
Loudness,Dolby NR,Sound,Cruise:Off
Volume position is about 2 V at speaker output with
following conditions.Playback the test tape VT721.
AM mode $999 \mathrm{kHz} / 62 \mathrm{~dB}, \mathrm{INT} / 400 \mathrm{~Hz}, 30 \%$ modulation signal on receiving.
FM mono mode $97.9 \mathrm{MHz} / 66 \mathrm{~dB}, \mathrm{INT} / 400 \mathrm{~Hz}, 22.5 \mathrm{kHz}$
deviation pilot off mono.
FM stereo mode $1 \mathrm{kHz}, 67.5 \mathrm{kHz}$ dev. pilot 7.5 kHz dev.

Output level $0 \mathrm{~dB}(1 \mu \mathrm{~V}, 50 \Omega /$ open terminal).

Tuner section BAND STEP
FM : 100kHz (Seek), 50 kHz (Manual)
AM : 9kHz step

Preset Memory Initialization

| Band | Preset Memory |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :--- |
|  | M1 | M2 | M3 | M4 | M5 | M6 |
| FM $(\mathrm{MHz})$ | 87.5 | 89.9 | 97.9 | 105.9108 .0 | 87.5 |  |
| AM $(\mathrm{kHz})$ | 153 | 216 | 603 | 999 | 1404 | 1620 |

## DUMMY LOAD

Exclusive dummy load should be used for AM and FM dummy load,there is a loss of 6 dB between SSG output and antenna input. The loss of 6 dB need not be considered since direct reading of figures are applied in this working standard.


## Arrangment of adjusting

Head amplifier board section (Reverse side)


## - Information for using a car audio service jig

1. We're advancing efforts to make our extension cords common for all car audio products.

Please use this type of extension cord as follows.
2. As a U-shape type top cover is employed, this type of extension cord is needed to check operation of the mechanism assembly after disassembly.
3. Extension cord : EXTKSRT002-18P ( 18 pin extension cord) For connection between mechanism assembly and main board assembly.
Check for mechanism driving section such as motor ,etc..

## Disassembly method

1. Remove the bottom cover.
2. Remove the front panel assembly.
3. Remove the top cover .
4. Install the front panel.
5. Confirm that current is being carried by connecting an extension cord jig.
Note
Available to connect to the CP701 connector when installing the front panel.

Extension cord
Cassette mechanism


EXTKSRT002-18P
to Cassette mechanism


| Item | Conditions | Adjustment and Confirmation methods | S.Values | Adjust |
| :---: | :---: | :---: | :---: | :---: |
| 1. Head azimuth adjustment | $\begin{aligned} & \text { Test tape: } \\ & \text { SCC-1659 } \\ & \text { VT703(10kHz) } \end{aligned}$ | Head height adjustment <br> ※ Adjust the azimuth directly. When you adjust the height using a mirror tape, remove the cassette housing from the mechanism chassis. After installing the cassette housing, perform the azimuth adjustment. <br> 1. Load the SCC-1659 mirror tape. Adjust with height adjustment screw $A$ and azimuth adjustment screw $B$ so that line $A$ of the mirror tape runs in the center between Lch and Rch in the reverse play mode. <br> 2. After switching from REV to FWD then to REV, check that the head position set in procedure 1 is not changed. (If the position has shifted, adjust again and check.) <br> 3. Adjust with azimuth adjustment screw $B$ so that line B of the mirror tape runs in the center between Lch and Rch in the forward play mode. <br> Head azimuth adjustment <br> 1. Load VTT724 (VT724) ( 1 kHz ) and play it back in the reverse play mode. Set the Rch output level to max. <br> 2. Load VTT703 (VT703) (10kHz) and play it back in the forward play mode. Adjust the Rch and Lch output levels to max, with azimuth adjustment screw B. In this case, the phase difference should be within $45^{\circ}$. <br> 3. Engage the reverse mode and adjust the output level to max, with azimuth adjustment screw C. <br> (The phase difference should be $45^{\circ}$ or more.) <br> 4. When switching between forward and reverse modes, the difference between channels should be within 3dB. (Between FWD $L$ and $R, R E V L$ and R.) <br> 5. When VTT721 (VT721) $(315 \mathrm{~Hz})$ is played back, the level difference between channels should be within 1.5 dB . |  | is at low position $B$ line <br> s at High position <br> hase <br> (45 ${ }^{\circ}$ ) |
| 2. Tape speed and wow flutter confirmation | Test tape: VTT712 <br> (3kHz) | 1. Check to see if the reading of the F, counter / wow flutter meter is within 3015~3045(FWD / REV), and less than $0.35 \%$ (JIS RMS). <br> 2. In case of out of specification, adjust the motor with a built-in volume resistor. | Tape speed: 3015 23045 Hz Wow flutter: less than $0.35 \%$ | Built-in volume resistor |
| 3. Play back frequency response confiramation | Test tape: VTT724 $(1 \mathrm{kHz})$ VTT739 $(63 \mathrm{~Hz} / 1 \mathrm{kHz} / 10 \mathrm{kHz})$ | 1. Play test tape VTT724, and set the volume position at 2 V . <br> 2. Play test tape VTT739 and confirm. $1 \mathrm{kHz} / 10 \mathrm{kHz}:-1 \pm 3 \mathrm{~dB}$, $1 \mathrm{kHz} / 63 \mathrm{~Hz}: 0 \pm 3 \mathrm{~dB}$, <br> 3. When 10 kHz is out of specification, it will be necessary to read adjust the azimuth. | Speaker out $1 \mathrm{kHz} / 63 \mathrm{~Hz}$ $: 0 \pm 3 \mathrm{db}$ $1 \mathrm{kHz} / 10 \mathrm{kHz}$ $:-1 \pm 3 \mathrm{db}$ |  |

The tuner section is of an adjustment-freedesign. In case the tuner is in trouble, replace the tuner pack.

## Descrption of major ICs

- TEA6320T-X (IC161) : E.volume
1.Pin layout

2.Block diagram

3.Pin functions

| Pin <br> No. | Symbol | I/O | Functions | Pin <br> No. | Symbol | I/O | Functions |
| :---: | :---: | :---: | :--- | :---: | :---: | :---: | :--- |
| 1 | SDA | I/O | Serial data input/output. | 17 | IAR | I | Input A right source. |
| 2 | GND | - | Ground. | 18 | IBR | I | Input B right source. |
| 3 | OUTLR | O | output left rear. | 19 | CAP | - | Electronic filtering for supply. |
| 4 | OUTLF | O | output left front. | 20 | ICR | I | Input C right source. |
| 5 | TL | I | Treble control capacitor left channel or <br> input from an external lequalizer. | 21 | Vref | - | Reference voltage (0.5Vcc) |
| 6 | B2L | - | Bass control capacitor left channel or <br> output to an external equalizer. | 22 | IDR | - | Not used |
| 7 | B1L | - | Bass control capacitor left channel. | 23 | QSR | O | Output source selector right channel. |
| 8 | IVL | I | Input volume 1. left control part. | 24 | ILR | I | Input loudness right channel. |
| 9 | ILL | I | Input loudness. left control part. | 25 | IVR | I | Input volume 1. right control part. |
| 10 | QSL | O | Output source selector. left channel. | 26 | B1R | - | Bass control capacitor right channel |
| 11 | IDL | - | Not used | 27 | B2R | O | Bass control capacitor right channel or <br> output to an external equalizer. |
| 12 | MUTE | - | Not used | 28 | TR | I | Treble control capacitor right channel or <br> input from an external equalizer. |
| 13 | ICL | I | Input C left source. | 29 | OUTRF | O | Output right front. |
| 14 | IMO | - | Not used | 30 | OUTRR | O | Output right rear. |
| 15 | IBL | I | Input B left source. | 31 | VCC | - | Supply voltage. |
| 16 | IAL | I | Input A left source. | 32 | SCL | I | Serial clock input. |

## SAA6579T-X(IC71):RDS Detector

1.Terminal Layout

2.Pin Function

| Pin <br> No. | Symbol | I/O | Function |
| :---: | :---: | :---: | :--- |
| 1 | QUAL | - | Non connect |
| 2 | RDDA | O | RDS data output |
| 3 | Vref | O | Reference voltage output |
| 4 | MUX | I | Multiplex signal input |
| 5 | VDDA | - | +5V Supply voltage for analog |
| 6 | GND | - | Ground for analog part (0V) |
| 7 | CIN | I | Sub carrier output of reconstruction filter |
| 8 | SCOUT | O | Ground for digital part (OV) |
| 9 | MODE | - | Ground for digital part (OV) |
| 10 | TEST | - | Ground for digital part (OV) |
| 11 | GND | - | Ground for digital part (OV) |
| 12 | VDD | - | +5V supply voltage for digital part |
| 13 | OSC1 | I | Oscillator input |
| 14 | OSCO | O | Oscillator output |
| 15 | T75 | - | Non connect |
| 16 | RDCL | O | RDS clock output |

3.Block Diagram


## LA4743K(IC301):Power AMP

1.Block diagram

2.Terminal layout

3.Pin function

| Pin No. | Symbol |  |
| :---: | :--- | :--- |
| 1 | TAB | Header of IC |
| 2 | GND | Power GND |
| 3 | OUTRR- | Outpur(-) for front Rch |
| 4 | STBY | Stand by input |
| 5 | OUTRR + | Output (+) for front Rch |
| 6 | VCC1/2 | Power input |
| 7 | OUTRF- | Output (-) for rear Rch |
| 8 | GND | Power GND |
| 9 | OUTRF+ | Output (+) for rear Rch |
| 10 | RIPPLE | Ripple filter |
| 11 | INRF | Rear Rch input |
| 12 | INRR | Front Rch input |
| 13 | SGND | Signal GND |
| 14 | INLR | Front Lch input |
| 15 | INLF | Rear Lch input |
| 16 | ONTIME | Power on time control |
| 17 | OUTLF+ | Output (+) for rear Lch |
| 18 | GND | Power GND |
| 19 | OUTLF- | Output (-) for rear Lch |
| 20 | VCC3/4 | Power input |
| 21 | OUTLR+ | Output (+) for front |
| 22 | MUTE | Muting control input |
| 23 | OUTLR- | Output (-) for front |
| 24 | GND | Power GND |
| 25 | NC | Non connection |

U UPD178018AGC-586 (IC701) : Main system control CPU ■ UPD178018AGC-604 (IC701) : Main system control CPU

1. Pin layout

2. Pin function

| Pin No. | Port Name | I/O | Descriptions |
| :---: | :---: | :---: | :---: |
| 1 | KEY 0 | 1 | Key input 0 |
| 2 | KEY 1 | 1 | Key input 1 |
| 3 | KEY 2 | I | Key input 2 |
| 4 | LEVEL | I | Level meter input |
| 5 | SM | 1 | S.meter level input |
| 6 | SQ | 1 | S. Quality level input |
| 7 | LCDCE | 0 | CE output to LCD driver |
| 8 | LCDDA | 0 | Data output to LCD driver |
| 9 | LCDSCK | 0 | Clock output to LCD driver |
| 10 | BUSI/O | 1 | I/O selector output for J-BUS, H : OUT, L: INPUT |
| 11 | OPEN | I | Door open detect input |
| 12 | BUSSI | 1 | J-BUS Data input |
| 13 | BUSSO | 0 | J-BUS Data output |
| 14 | BUSSCK | I/O | J-BUS Clock in/output |
| 15 | NC | - | Non connect |
| 16 | NC | - | Non connect |
| 17 | NC | - | Non connect |
| 18 | NC | - | Non connect |
| 19 | INLOCK | - | Non connect |
| 20 | NC | - | Non connect |
| 21 | GNDPORT | - | Port GND |
| 22 | VDDPORT | - | Port Vdd |
| 23 | NC | 0 | Non connect |
| 24 | AFCK | 0 | AF check output, L: AF check |
| 25 | MONO | 0 | Monaural on /off selecting output, H:mono on |
| 26 | FM/AM | 0 | FM/AM switching output L:FM H:AM |
| 27 | SEEK/STP | 0 | Auto seek /stop selecting output, H: Seek, L:Stop |
| 28 | NC | 1 | Pulse signal input port for Cruise control |
| 29 | IFC | 1 | FM/AM midle frequency counter input |
| 30 | VDDPLL | - | PLL Vdd |
| 31 | FMOSC | 1 | FM/AM limited generator frequency input |
| 32 | NC | - | None connect |
| 33 | GNDPLL | - | PLL GND |
| 34 | AMEO | 0 | AM error out output |
| 35 | FMEO | 0 | FM error out output |
| 36 | IC | - | GND |
| 37 | SD/ST | 1 | Station detector, Stereo signal input, H:Find Station, L:Stereo |
| 38 | STAGE0 | 1 | Pull up |
| 39 | NC | - | Non connect |
| 40 | MOTOR | O | Main motor output |

## ■ UPD178018AGC-551 (IC701) : Main system control CPU <br> ■ UPD178018AGC-551 (IC701) : Main system control CPU

| Pin No. | Port Name | I/O | Descriptions |
| :---: | :---: | :---: | :---: |
| 41 | FF/REW | 1 | Output for input signal level switching for MS L: FF,REW H: PLAY |
| 42 | F/R | O | FWD,REV running direction switch signal input |
| 43 | DOLBY | O | Dolby on "H" output |
| 44 | MSIN | I | MS input |
| 45 | I2CCLK | 0 | 12C information clock output |
| 46 | I2CDAO | O | I2C information data output |
| 47 | I2CDAI | 1 | I2C information clock input |
| 48 | REEL | O | Switch for detecting tape end position |
| 49 | SUBMO1 | 1 | Sub motor clock direction input |
| 50 | SUBMO2 | 0 | Sub motor clock opposite detection drive output |
| 51 | MODE | 0 | Mechanism mode position detection input |
| 52 | TAPEIN | 0 | Cassette in detection input H: cassette in L: cassette out |
| 53 | STANDBY | 1 | Standby position detection input H: eject side L: operation side |
| 54 | NC | - | Non connect |
| 55 | NC | - | Non connect |
| 56 | NC | - | Non connect |
| 57 | NC | - | Non connect |
| 58 | NC | - | Non connect |
| 59 | BEEP | 0 | Touch tone output |
| 60 | MUTE | 0 | Mute output, L: mute on |
| 61 | PCNT | 0 | Power ON /OFF switching output, H : power on |
| 62 | TELMUTE | I | Telephone mute signal detection input |
| 63 | DIMIN | 1 | Dimmer signal detection input L: dimmer |
| 64 | DIMOUT | 0 | Dimmer control output, Dimmer off L output |
| 65 | ENC1 | I | Rotary volume signal 1 input Power save: L |
| 66 | ENC2 | I | Rotary volume signal 2 input |
| 67 | ACCDET | 1 | Power save 1 Working togethe ACC Power save: L |
| 68 | POWER | O | Power save 2, Working together Back up by H input, stop mode |
| 69 | RDSSCK | I | Clock input for RDS |
| 70 | RDSDA | 1 | RDS data input |
| 71 | REMOCON | 1 | Remocom input |
| 72 | DETACH | 1 | Detach signal input H: Power save |
| 73 | J-BUSINT | 1 | Cut-in input for J-BUS signal |
| 74 | REGCPU | - | Regulator for CPU power supply, Connect the GND with0.1 $\mu \mathrm{F}$. |
| 75 | GND | - | Ground |
| 76 | X2 | - | Connecting the crystal oscillator for system clock |
| 77 | X1 | 1 | Connecting the crystal oscillator for system clock |
| 78 | REGOSC | - | Regulator for oscillator circuit.Connect the GND with $0.1 \mu \mathrm{~F}$. |
| 79 | VDD | - | Vdd |
| 80 | RESET | - | Pull up |

## - HD74HC126FP-X (IC801) : Buffer

1.Terminal layout


## 3.Pin function

| Input |  | Outout |
| :---: | :---: | :---: |
| C | A | Y |
| L | X | Z |
| H | L | H |
| H | H | L |

2.Block diagram


■ HA13164(IC901):REGULATOR
1.Terminal layout

2.Block diagram


UNIT R: $\Omega$
note1) TAB (header of IC)
connected to GND
3.Pin function

| Pin No. | Symbol | Function |
| :---: | :--- | :--- |
| 1 | EXTOUT | Output voltage is VCC-1 V when M or H level applied to CTRL pin. |
| 2 | ANTOUT | Output voltage is VCC-1 V when M or H level to CTRL pin and H level <br> to ANT-CTRL. |
| 3 | ACCIN | Connected to ACC. |
| 4 | VDDOUT | Regular 5.7V. |
| 5 | SW5VOUT | Output voltage is 5V when M or H level applies to CTRL pin. |
| 6 | COMPOUT | Output for ACC detector. |
| 7 | ANT CTRL | L:ANT output OFF , H:ANT output ON |
| 8 | VCC | Connected to VCC. |
| 9 | BATT DET | Low battery detect. |
| 10 | AUDIO OUT | Output voltage is 9V when M or H level applied to CTRL pin. |
| 11 | CTRL | L:BIAS OFF, M:BIAS ON, H:CD ON |
| 12 | CD OUT | Output voltage is 8V when H level applied to CTRL pin. |
| 13 | ILM AJ | Adjustment pin for ILM output voltage. |
| 14 | ILM OUT | Output voltage is 10V when M or H level applies to CTRL pin. |
| 15 | GND | Connected to GND. |

VICTOR COMPANY OF JAPAN, LIMITED
MOBILE ELECTRONICS DIVISION

